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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/692,448	10/23/2003	Joerg Kirchner	TI-34407	8026	
23494	7590 12/20/2005		EXAM	INER	
TEXAS INSTRUMENTS INCORPORATED			STERRETT, JEFFREY L		
P O BOX 655474, M/S 3999 DALLAS, TX 75265			ART UNIT	PAPER NUMBER	
			2838		
			DATE MAILED: 12/20/200	DATE MAILED: 12/20/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)	
Office Action Summary		10/692,448	KIRCHNER ET AL.	
		Examiner	Art Unit	
		Jeffrey L. Sterrett	2838	
The MAILING DATE of Period for Reply	this communication app	pears on the cover sheet with the c	correspondence address -	-
THE MAILING DATE OF TH - Extensions of time may be available u after SIX (6) MONTHS from the mailin - If the period for reply specified above - If NO period for reply is specified above - Failure to reply within the set or extend	IS COMMUNICATION. Inder the provisions of 37 CFR 1.1 Index of this communication. Index is than thirty (30) days, a reply Index is the maximum statutory period with the period for reply will, by statute Index is the maximum start the mailing than three months after the mailing	Y IS SET TO EXPIRE 3 MONTH(36(a). In no event, however, may a reply be tir y within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE g date of this communication, even if timely filed	nely filed /s will be considered timely. Ithe mailing date of this communica (D) (35 U.S.C. § 133).	ition.
Status				
1) Responsive to commu	nication(s) filed on 15 N	ovember 2005.		
2a)⊠ This action is FINAL .	· · · <u> </u>	action is non-final.		
3) Since this application i	s in condition for allowa	nce except for formal matters, pro	osecution as to the merits	s is
closed in accordance v	vith the practice under E	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.	
Disposition of Claims				
4)⊠ Claim(s) <u>1-11</u> is/are pe	nding in the application.			
4a) Of the above claim	s) is/are withdra	wn from consideration.		
5) Claim(s) is/are	allowed.			
6)⊠ Claim(s) <u>1-11</u> is/are re	ected.			
7) Claim(s) is/are	objected to.			
8) Claim(s) are sul	oject to restriction and/o	r election requirement.	•	
Application Papers				
9) ☐ The specification is objection is objection is objective.	ected to by the Examine	r.		
10)⊠ The drawing(s) filed on	23 October 2003 is/are:	a)∏ accepted or b)⊠ objected	I to by the Examiner.	
Applicant may not reques	t that any objection to the	drawing(s) be held in abeyance. Se	e 37 CFR 1.85(a).	
Replacement drawing sh	eet(s) including the correct	ion is required if the drawing(s) is ob	jected to. See 37 CFR 1.12	1(d).
11)☐ The oath or declaration	is objected to by the Ex	caminer. Note the attached Office	Action or form PTO-152	
Priority under 35 U.S.C. § 119				
a) All b) Some * c) 1. Certified copies 2. Certified copies 3. Copies of the ce application from	None of: of the priority document of the priority document rtified copies of the prior the International Bureau	s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	ion No ed in this National Stage	
	u Onice action for a fist	of the certified copies not receive	tu.	
Attachment(s)		n □ 1-4 1 - 6	(DTO 442)	
 Notice of References Cited (PTO- 2) Notice of Draftsperson's Patent Dr 		4) ∭ Interview Summary Paper No(s)/Mail D		
3) Information Disclosure Statement(Paper No(s)/Mail Date	- ·		Patent Application (PTO-152)	

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1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

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- 2. New corrected drawings in compliance with 37 CFR 1.121(d) are required in this application because the proposed amendments to figures 1 and 2 are acceptable but they MUST be submitted as formal drawings containing the corrections (the current sheets, erroneously labeled "replacement sheet" and really more appropriately labeled "annotated sheet", are informal due to the hand drawn lines that are supposed to cover the dashed lines with solid lines). Applicant is advised to employ the services of a competent patent draftsperson outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The corrected drawings are required in reply to the Office action to avoid abandonment of the application. The requirement for corrected drawings will not be held in abeyance.
- 3. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed. A descriptive title clearly indicative of the invention based on the concept of "INDUCTOR TYPE REGULATOR USING MOSFET'S" is suggested.

4. Claims 1-8 are objected to because in line 14 of claim 1 "said back gate" lacks proper antecedent basis. It is suggested that "said" be changed to –the- as is currently recited in line 12 of the other independent claim, claim 9.

Appropriate correction is required.

- 5. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 6. Claims 1-11 are rejected under 35 U.S.C. 102(b) as being anticipated by D'Angelo et al (US 5,929,615).

D'Angelo et al discloses a DC/DC converter comprising an input to which an input voltage (Vin) is applied, an inductance (36) with a first terminal connected to the input, a first controllable NMOS FET switch (46) connected between the second terminal (38) of the inductance and a grounded reference point, a second controllable PMOS FET switch (34) connected between the second terminal (38) of the inductance and the converter output (Vout), a further controllable PMOS FET switch (42) connected between the back gate of the second switch and the converter output (Vout), an output storage capacitor (48), and a regulator circuit (44-58) configured to regulate the converter output voltage (Vout) to a predetermined value by controlling the first and second controllable switches.

7. Applicant's arguments filed November 15, 2005 have been fully considered but they are not persuasive.

In response to the remarks concerning the disclosure of D'Angelo et al, whether or not D'Angelo et al discloses a control unit 44 that temporarily suspends switch-mode

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operation of the entire voltage regulator 32 is moot. Although this may or may not be a valid patentable distinction in the present <u>specification</u> over the disclosure of D'Angelo et al, nevertheless the language of the present <u>claims</u> never implicitly or explicitly sets forth any such limitation. Thus the claims remain rejected under 35 U.S.C. 102(b) as being anticipated by D'Angelo et al.

8. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeffrey L. Sterrett whose telephone number is (571) 272-2085. The examiner can normally be reached on Monday-Thursday & 8:00am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on (571) 272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Business Center (EBC) at 866-217-9197 (toll-free).

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Jeffrey L. Sterrett Primary Examiner Art Unit 2838

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